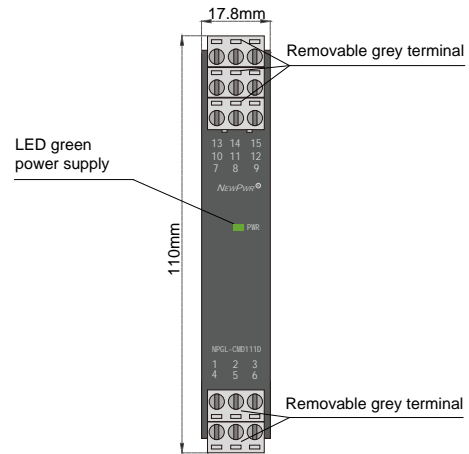


NPGL-CMD111D

Dual input, dual output

Input: 4 ~ 20 mA
Output: 4 ~ 20 mA

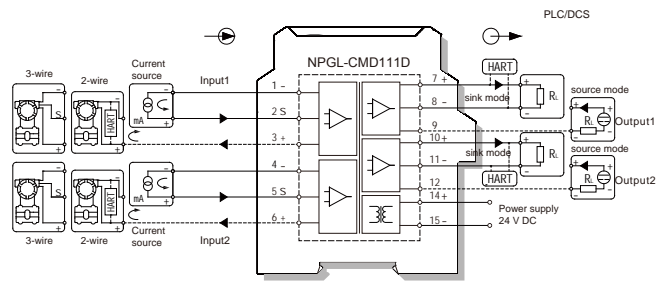
This isolator detects loop current and converts it into current signals, and also provides transmitters with power in the field area. It allows transmission of HART communication signals. The input, output, and power supply are galvanically isolated from each other.



Parameters

Power supply:	18 V DC ~ 60 V DC (Reverse power protection)
Power dissipation:	2.5 W
Input signal:	4 ~ 20 mA, HART
Line resistance:	approx. 50 Ω
Available voltage:	open-circuit voltage ≤ 25 V voltage: ≥ 21 V at 20 mA
Output signal:	4 ~ 20mA (sink/source), HART
Load resistance:	source: $R_L \leq 550\Omega$ sink: $R_L < [(U-3)/0.02]\Omega$; U: Loop power supply
Compensation accuracy:	0.1%F.S.
Temperature drift:	30 ppm/°C
Response time:	≤ 2 ms
Electromagnetic compatibility:	IEC 61326-3-1
Dielectric strength:	≥ 1500 V AC (Input/Output/Power supply)
Insulation resistance:	≥ 100 MΩ (Input/Output/Power supply)
Operation temperature:	-20 °C ~ +60 °C
Storage temperature:	-40 °C ~ +80 °C
Dimension:	17.8 mm (W) × 110 mm (H) × 117 mm (D)

Wiring diagram



Other ordering information

Type	Input	Output1	Output2	Power supply
NPGL-CMD122D	4 ~ 20 mA	1 ~ 5 V	1 ~ 5 V	Terminal
NPGL-CMD666D	0 ~ 20 mA	0 ~ 20 mA	0 ~ 20 mA	Terminal